

FIG. 1

200 →

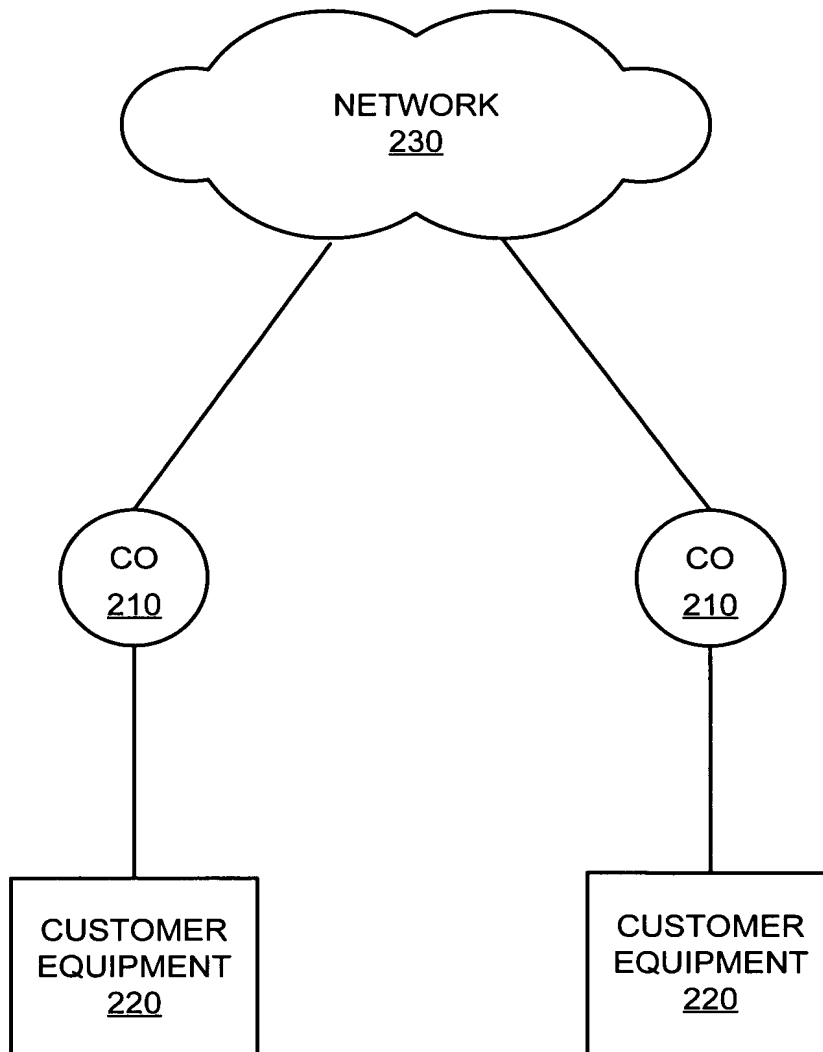


FIG. 2

FIG. 3 is a block diagram of a system 300. The system 300 includes an input device 360, an output device 370, a communication interface 380, a processor 310, main memory 320, ROM 330, and a storage device 340. The input device 360, output device 370, and communication interface 380 are connected to a bus 350. The processor 310, main memory 320, ROM 330, and storage device 340 are also connected to the bus 350.

300 →

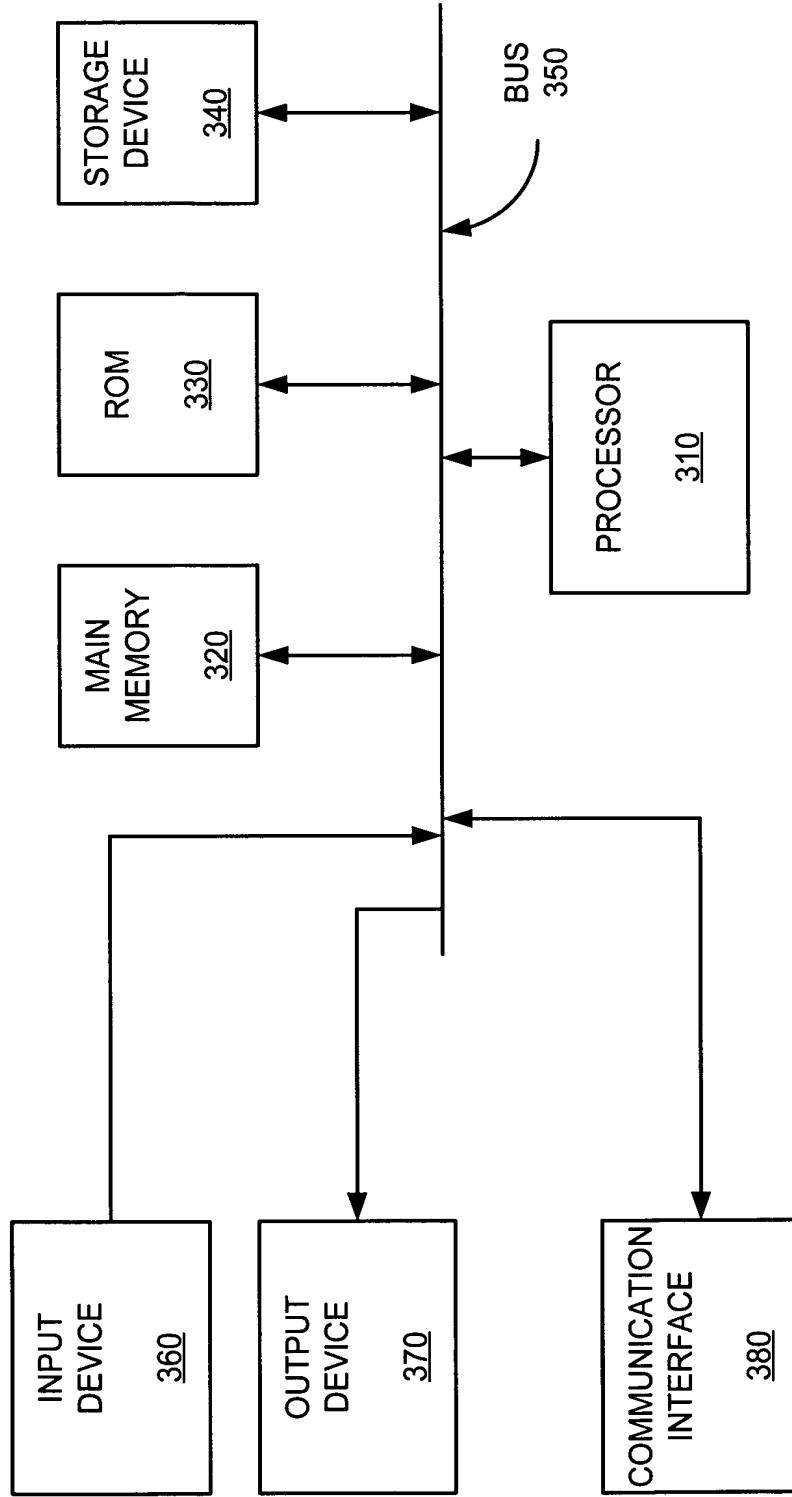


FIG. 3

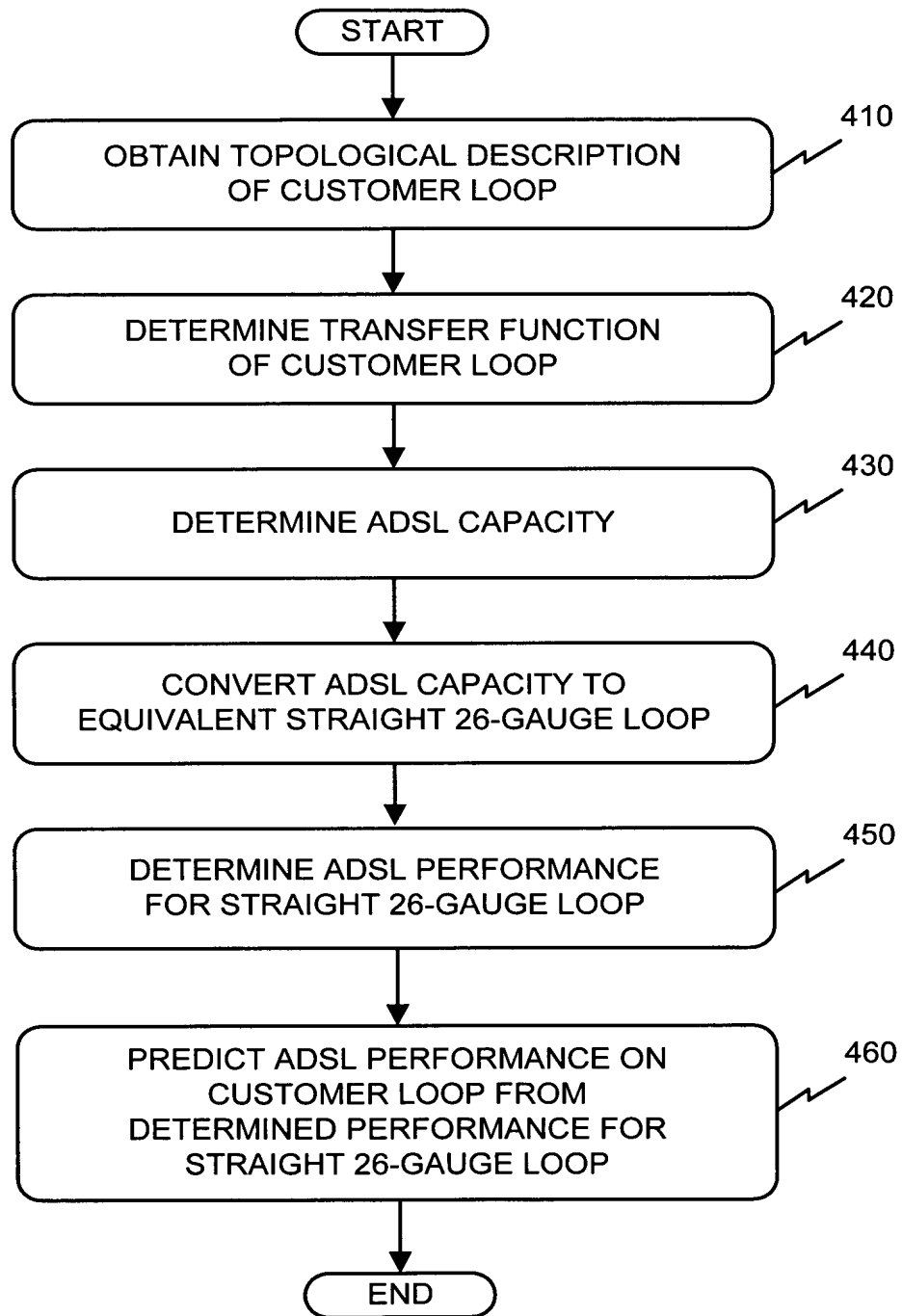


FIG. 4

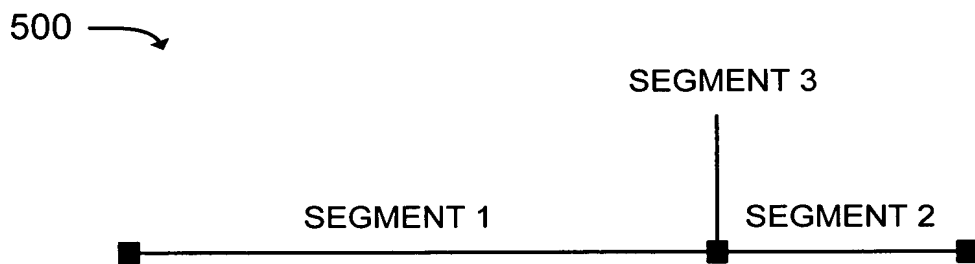


FIG. 5

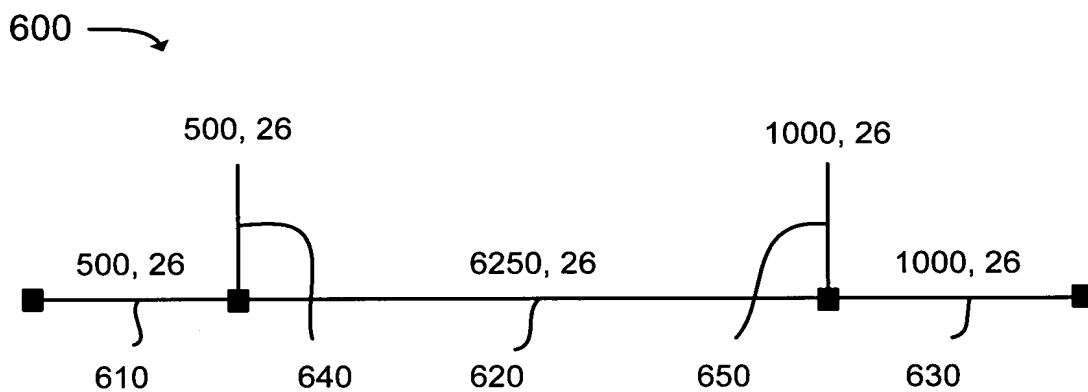


FIG. 6

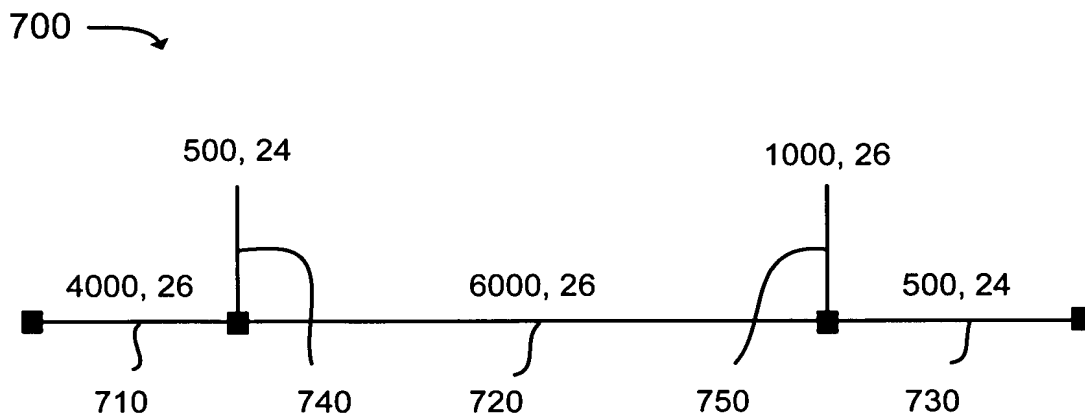


FIG. 7

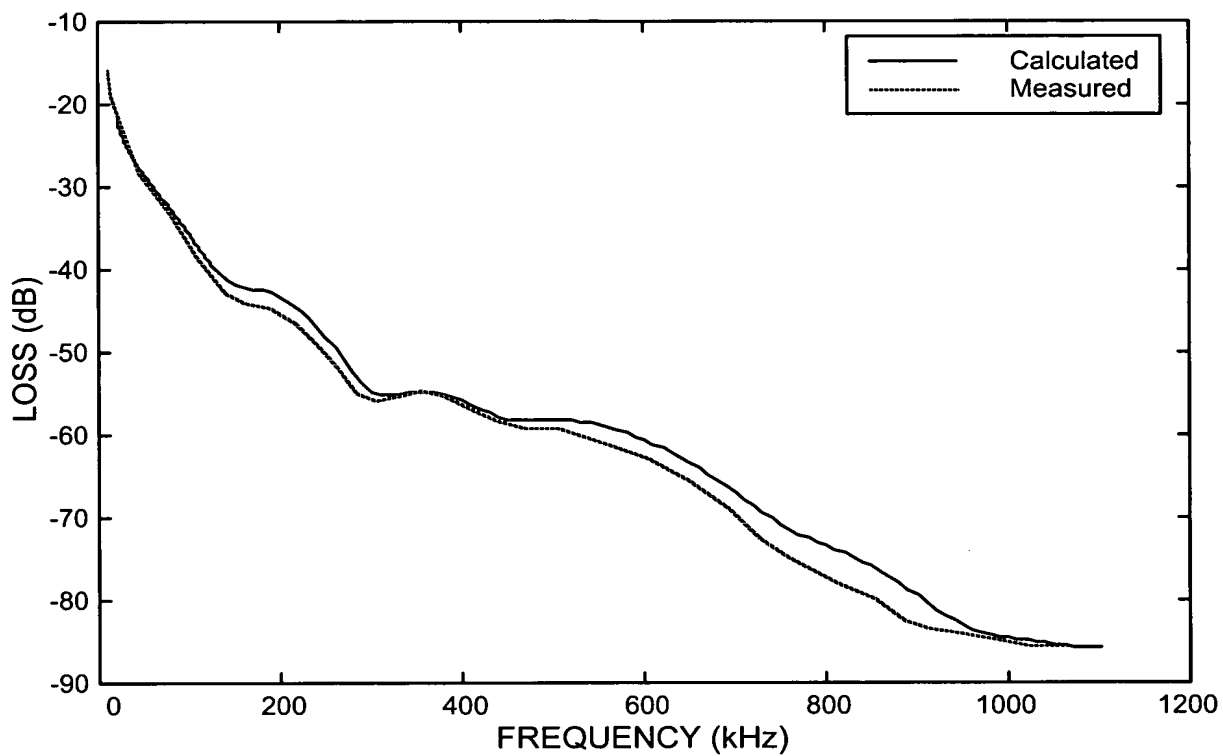


FIG. 9

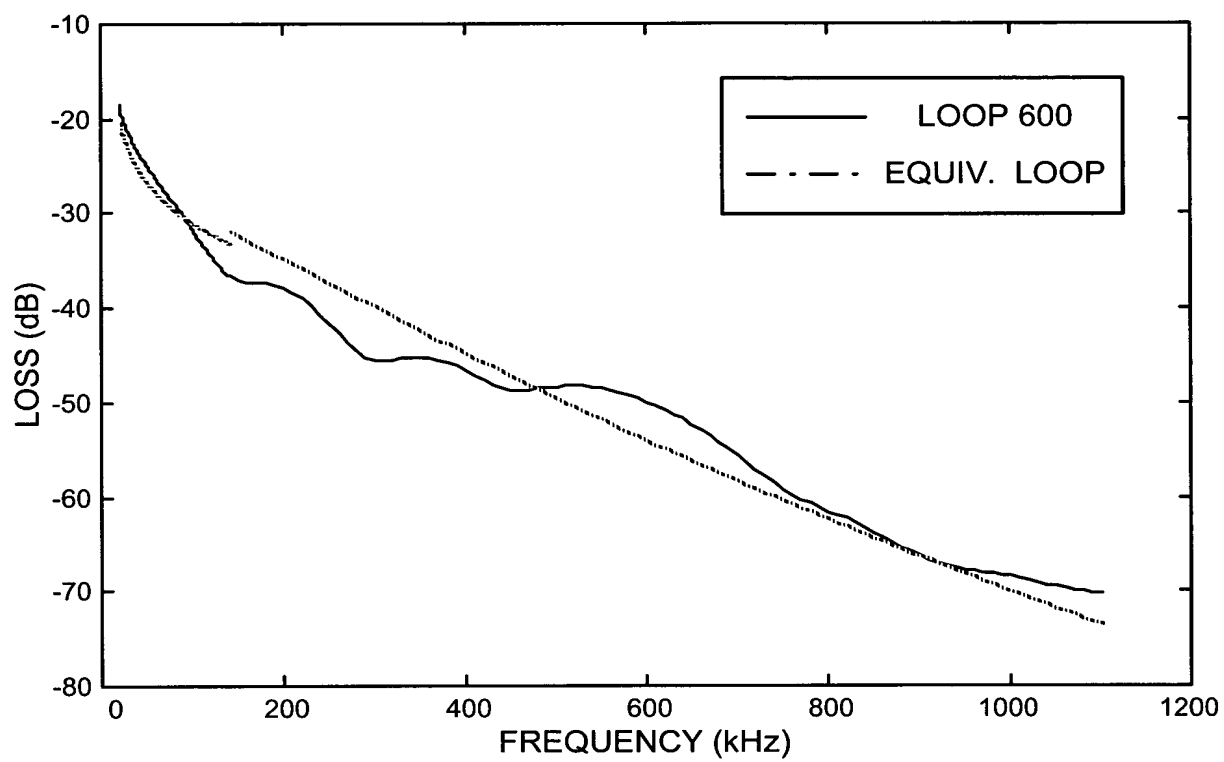


FIG. 10

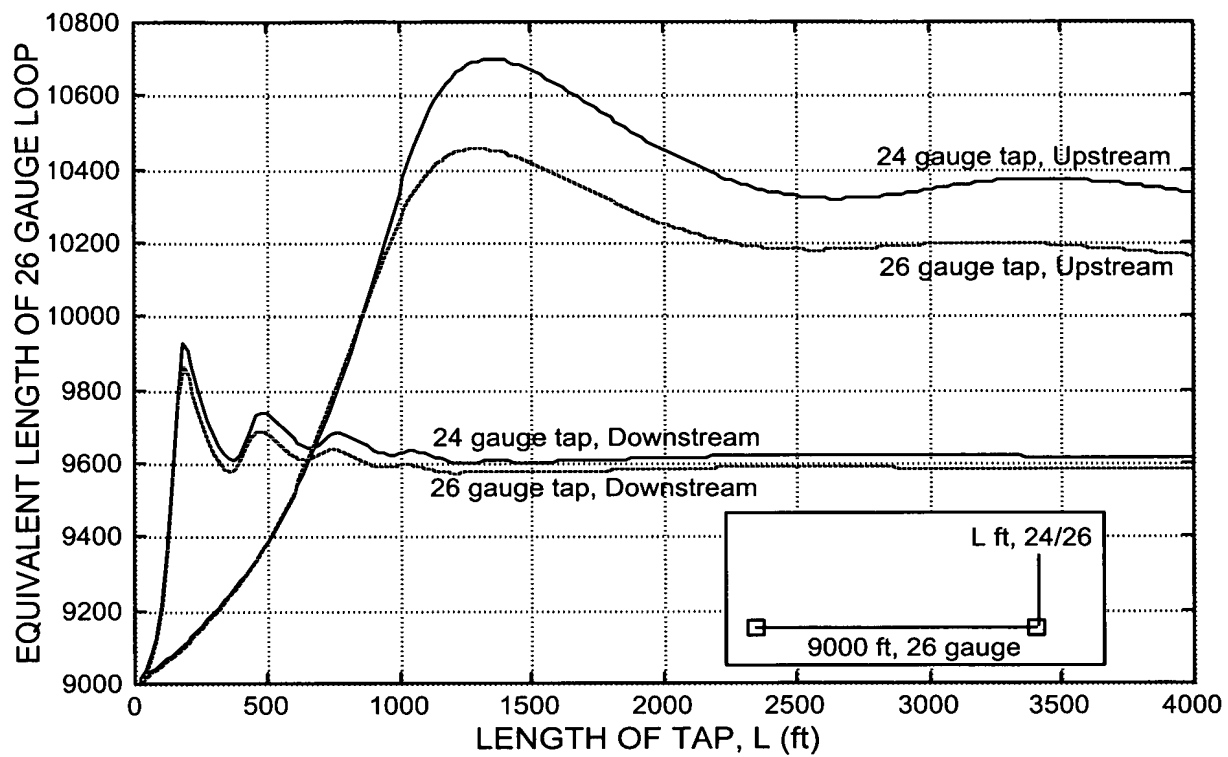


FIG. 11

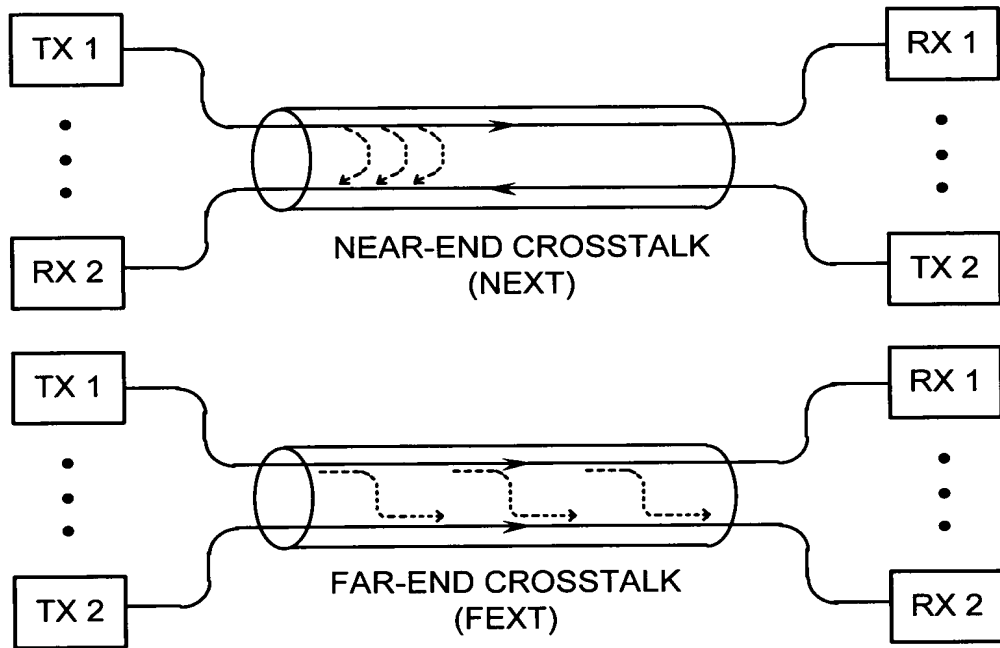


FIG. 12

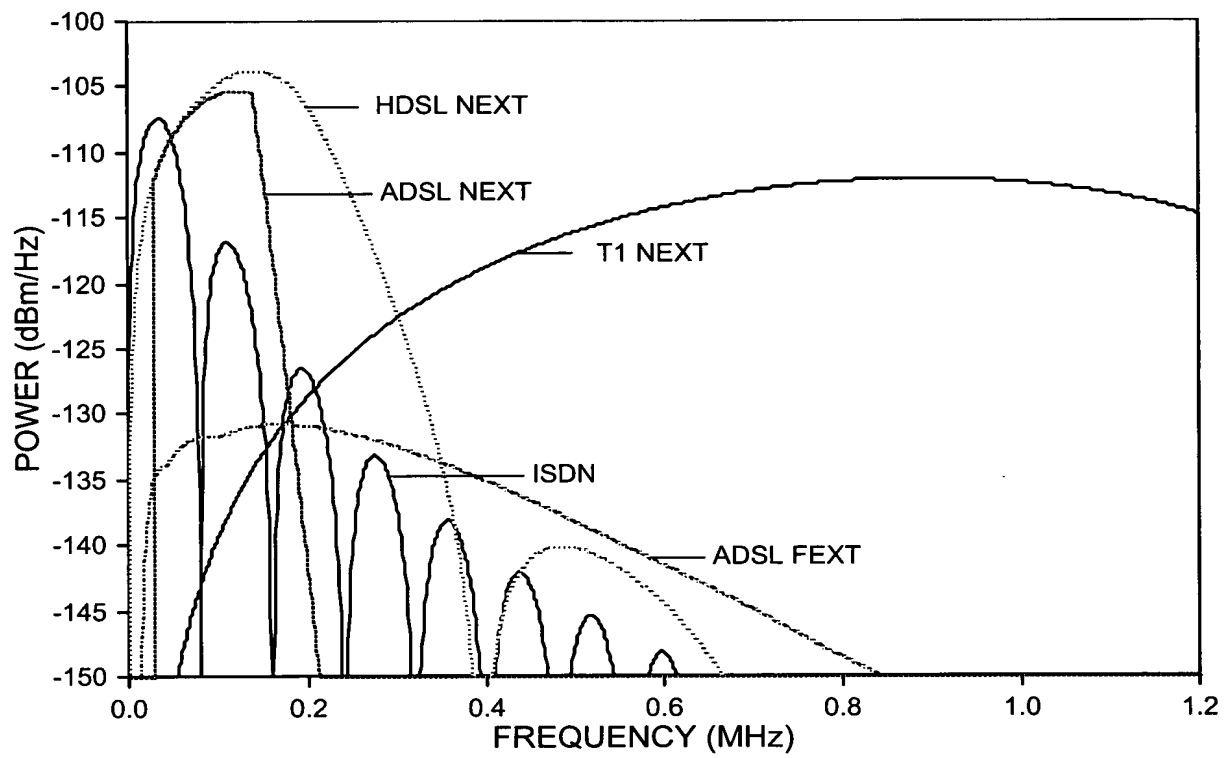


FIG. 13